

Notice of References Cited

Application/Control No

09/893,023

Applicant(s)/Patent Under
Reexamination
O'MAHONY ET AL

Examiner

A. Sefer

Art Unit

2826

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,397,375	05-2002	Block et al	716/14
	B	US-5,761,253	06-1998	Fujita et al	375/354
	C	US-5,521,541	05-1996	Okamura	327/297
	D	US-6,420,663	07-2002	Zelikson et al	174/261
	E	US-2002/0083359	06-2002	Dow	713/500
	F	US-5,387,885	02-1995	Chi	333/100
	G	US-2002/0190775	12-2002	Magoshi	327/295
	H	US-6,184,736	02-2002	Wissel et al	327/295
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 2-158165	06-1990	Japan	Sano et al	
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Restle et al. "Desgning the Best Clock Distribution Network" IEEE Symposium on VLSI Circuits Digest of Techincal Papers, 1998
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action (See MPEP § 707.05(a))
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.